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TRANSMITTAL FORM (to be used for all correspondence after initial filing) BOX: NON-FEE AMENDMENT Express Mail Receipt No.	Application / Conf. No.	10/090,239 / 6539
	Filing Date	March 01, 2002
	First Named Inventor	Atul V. Ghia
	Examiner Name	Unknown
	Group Art Unit	2819
	Issue Fee Batch No.	
	Attorney Docket Number	X-1061 US

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<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Terminal Disclaimer	RECEIVED JUN 03 2002
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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Atul V. Ghia et al.
Assignee: Xilinx, Inc.
Title: "Low Jitter Clock for a Physical Media Access Sublayer on a Field Programmable Gate Array"
Serial No.: 10/090,239 File Date: 03-01-02
Examiner: Unknown Art Unit: 2819
Docket No.: X-1061 US Conf. No.: 6539

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the twelve (12) references listed in the attached Form PTO-1449. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

H. C. Chan
Attorney for Applicants
Reg. No. 35,477

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Pat Slaback
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